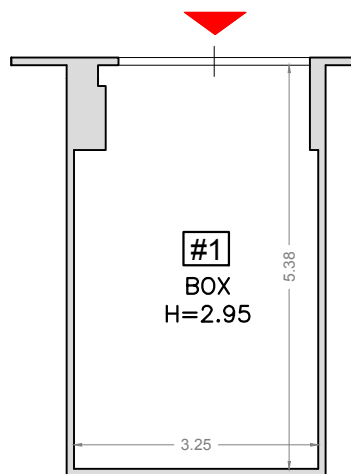


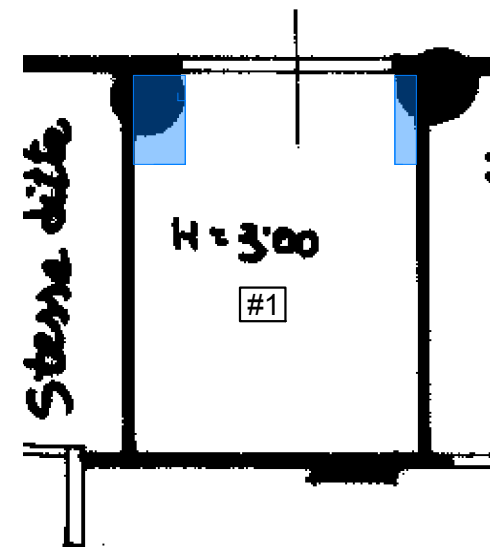
Hand-drawn diagram of a 20-bit bus system. The top row shows 10 multiplexers labeled (mux 64) to (mux 55) with inputs 19 down to 10. The bottom row shows 10 multiplexers labeled (mux 46) to (mux 59) with inputs 2 down to 9. A red box highlights the (mux 48) input. A 20-bit bus is shown at the bottom, with a small square indicating a specific bit position.

SCALA 1:100



Ambiente	Superficie utile	Superficie lorda
#1	16,9 mq	18,4 mq

SCALA 1:100



	elementi riportati in planimetria catastale ma non presenti nello stato di fatto
	elementi presenti nello stato di fatto ma non riportati in planimetria catastale

Esecuzione Immobiliare n. 290/2024 RGE

NOVEMBRE 2024

dott. ing. Davide PORTOLANO